Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	28	write$1through with snoop	USPAT	OR	OFF	2005/06/30 16:58
S2	1	processor with (low$2 adj power) with (memory adj read)	USPAT	OR	OFF	2005/06/30 16:46
S3	4	write$1through with snoop with processor	USPAT	OR	OFF	2005/06/30 16:56
S4	2	write$1through with snoop with processor	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 16:56
S5	458	write$1through adj cach$5	USPAT	OR	OFF	2005/06/30 18:13
S6	6	(write$1through adj cach$5) same (processor with snoop$3)	USPAT	OR	OFF	2005/06/30 17:09
S7	3	(write$1through adj cach$5) same (processor with snoop$3)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 17:05
S8	30	(write$1through adj cach$5) near2 (advantage$$1 architecture design)	USPAT	OR	OFF	2005/06/30 17:49
S9	1	(write$1through adj cach$5) near2 (advantage$1 architecture design)	EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 17:22
S10	8	(write$1through adj cach$5) same (read with memory with snoop$3)	USPAT	OR	OFF	2005/06/30 17:55
S11	1	(write$1through adj cach$5) same (read with memory with snoop$3)	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2005/06/30 17:55
S12	37	write$1through adj cach$5 near2 (architecture design technique)	USPAT	OR	OFF	2005/06/30 18:17
S13	9	write$1through adj cach$5 near2 (architecture design technique) same read	USPAT	OR	OFF	2005/06/30 18:17